

REV LTR	DESCRIPTION	DATE	APPVD.
-	Orig. Release	01/16/10	JSN
A	Revised per ECN 2012-1	11/09/12	JSN
B	Revised per ECN 2014-1	03/26/14	JSN
C	Revised per ECN 2016-2	10/21/16	JSN

## FTTC14 Series

CMOS/Sinewave/Clipped sinewave Output  
TCXO/VCTCXO

FOR SPACE APPLICATIONS

1M Hz to 800 MHz

( 20x13 mm, DIP14, 3.3 V/5.0V )

( Refer to Page 5 for Models with Reduced Screening & QCI )

1. SCOPE: FTTC14 series, CMOS/Sinewave output, high reliability hybrid microcircuit crystal oscillators are designed, produced and tested by Frequency Technology, Inc. as MIL-PRF-55310, Class "S" equivalent devices for use in advanced military, avionics and space applications. These devices are of hybrid microcircuit technology conforming to MIL-PRF-55310, Type 1, Class 2 oscillators.

2. APPLICABLE DOCUMENTS:

MIL-PRF-55310E	Oscillator, Crystal Controlled, General Specifications for
MIL-PRF-38534G	Hybrid Microcircuits, General Specifications for
MIL-STD-883E	Test Methods and Procedures for Microelectronics

3. REQUIREMENTS:

3.1 General: The individual item requirements shall be as specified herein.

3.2 Package: Ceramic, 90% Min. AL<sub>2</sub>O<sub>3</sub>. Thermal Resistance,  $\theta_{JC}$ : 50 °C / Watt.

3.2.1 Termination Finish: 1.3  $\mu$ m minimum gold plate over 2.0  $\mu$ m minimum nickel plate. Hot Solder tinning with Sn60/Pb40 solder per MIL-PRF-55310 is optional at an additional cost.

3.2.2 Weight: 15.0 Gms Max.

3.2.3 Reflow Soldering: Reflow soldering at 260 °C for 10 seconds shall not degrade the performance.

3.3 Hermeticity: Resistance welded, hermetically sealed, leak rate of 1(10)<sup>-8</sup> atm-cc/s Max.

3.4. Marking: As a minimum, the parts shall be marked with Fre-tech P/N and date code

3.5 Absolute Maximum Ratings: Unless otherwise specified, absolute maximum ratings shall be as follows:

Supply Voltage	-0.5 to +7.0 VDC
Operating Free-Air Temperature Range	-40°C to +85 °C
Storage Temperature	-55°C to +125°C

3.6 Electrical Characteristics: See Table I

3.6.1 Total Dose Radiation: Hybrid Microcircuit Crystal Oscillators shall be capable of meeting the electrical characteristics of Para. 3.6 after being exposed to total ionizing dose radiation of 100 krads as per MIL-STD-883, method 1019.

3.7 Hybrid Elements:

3.7.1 Quartz Crystals: High grade cultured quartz crystal shall be used. As an option, Fre-tech will use premium Q swept quartz crystal at an additional charge, refer to part numbering example in paragraph 6 to specify swept quartz crystal. Crystal element evaluation shall be in accordance with MIL-PRF-55310.

3.7.2 Crystal Mounting: Crystal element shall be mounted at 4 points in such a manner as to provide adequate ruggedness and performance under extreme environments specified herein.

- 3.7.3 Passive Elements: Established Reliability (ER) QPL components, failure level R minimum shall be used or element lot evaluation shall be as per MIL-PRF-55310, class S, or MIL-PRF-38534, Appendix C, Class K as applicable.
- 3.7.4 Microcircuit die shall be from lots that have passed the element evaluation per MIL-PRF-55310, Appendix B, Level S, except testing per Subgroup 5 is omitted. Subgroup 5 testing is circuit configuration dependent, therefore, it is more effectively performed at the oscillator level as explained in Paragraph 3.7.4 herein.
- 3.7.5 For Output Frequency up to 90 MHz, microcircuit die used in the oscillator shall be from NSC/FC 54ACT family and must be from wafer lot that has been successfully tested in the oscillator for ionizing radiation of up to 100 krad. Fre-tech has also performed SET & SEL testing on the microcircuit die. Test reports are available on request. For output frequencies above 90 MHz, the microcircuit die shall be from 0,8 μm BiCMOS Si family and must be from wafer lots that have been successfully tested in the oscillator for ionizing radiation of up to 100Krad and is known to be Single Event Latch-up immune for LET of up to 95 Mev-cm<sup>2</sup>/mg.
- 3.7.6 Workmanship, Rework and Process controls shall be in accordance with the requirements of MIL-PRF-55310.
- 3.7.7 Lot Traceability: Production lot for these oscillators shall be homogenous. Each element used in the production lot shall be traceable to a single lot. Swept quartz shall be traceable to the quartz bar, and its applicable processing details.

4. Quality Assurance Provisions: The quality assurance provisions shall be per MIL-PRF-55310, except as specified herein.

4.1 100% Screening: The 100% screening shall be performed as per Table II. PDA requirements for nondestructive bond pull and burn-in shall be as specified below.

4.2 PDA for Nondestruct Bond Pull: Unless otherwise specified, PDA shall be 2% of total number of wires or 1 wire whichever is greater.

4.3 PDA for Burn-in: Unless otherwise specified, PDA for burn-in shall be 2% or 1 oscillator whichever is greater and shall be applicable to +23 °C and/or +25 °C static tests only. In addition Delta Calculation shall be performed after Burn-in and shall count for PDA. All measured values for Delta Calculation shall be recorded. Parts that exceed the specified delta limits shall be rejected and be counted for PDA. Delta Calculation shall be performed at 3.3 VDC for the following parameters:

Input Current	10% change Maximum
Output High Level	10% change Maximum
Output Low Level	0.1V change Maximum

4.4 Group A inspection shall be in accordance with MIL-PRF-55310 for product level S.

4.5 Group B inspection ( 30 day aging ) shall be in accordance with MIL-PRF-55310 for product level S. In order to expedite delivery, by customer request, the aging test can be ended after 15 days if the amount of frequency aging is less than 50% of the 30 day specification limit.

4.6 Oscillators shall be capable of meeting group C inspection per MIL-PRF-55310. Generic group C inspection data on similar parts may be used to satisfy this requirement. When specified by the Customer, Fre-tech will perform Group C testing at an additional charge.

4.7 Inspection and Test Data: Unless otherwise specified in the purchase order, the following Inspection and test data documentation shall be supplied with the parts.

Certificate of Conformance  
Summary of Class "S" screening Test Results  
PDA Calculations for Non-Destruct Bond Pull and Burn-in  
Summary of Elements Lot Traceability  
Electrical Tests before and after Burn-in  
Group A Inspection Summary  
Group B (30 day Aging) Data  
Radiographic Inspection Certificate

4.8 The following test and inspection options are available at customer request.

Customer Source Inspection for Pre-Cap and Final  
Group C Inspection per MIL-PRF-55310 on 4 or 8 units  
DPA (Destructive Physical Analysis)  
Life Test per MIL-STD-883, Method 1005, 1000 Hrs. at +125 °C  
MIL-PRF-38534, Group B Inspection  
MIL-PRF-38534, Group C Inspection

5.0 Preservation, Packaging and Packing: The oscillators shall be clean, dry and packaged in a manner to provide adequate protection against electrostatic discharge, corrosion, deterioration and physical damage during shipment.

6.0

HOW TO ORDER

FTTC14	-	3		5		D		H		T		B	/	100
		Voltage		Frequency/ Temperature Stability		Temperature		Output Logic		Pull range( for VCTCXO)		Grade(Screening Level)		Frequency
		3=+3.3V		3= ± 0.3ppm		A= 0°C to +50°C		H= HCMOS/TTL		T= TCXO		N=No Screening		MHz
		5=+5.0V		5= ± 0.5ppm		B= -20°C to +70°C		S = Clipped Sinewave		V= VCTCXO		I= Industrial Std		
				10= ± 1.0ppm		C= -30°C to +70°C		Z = Sinewave				M= MIL-STD-883		
				15= ± 1.5ppm		D= -40°C to +85°C						B= MIL-PRF-55310,level B		
				20= ± 2.0ppm								S= MIL-PRF-55310,level S		
				25= ± 2.5ppm								V= MIL-PRF-55310,level S ,50krad(Si) total dose		
				30= ± 3.0ppm								R= MIL-PRF-55310,level S ,100krad(Si) total dose		
				50= ± 5.0ppm										

P/N Example: FTTC14 -310DZTV- 50.000 MHz = 50.000 MHz, Class “V” Oscillator, ± 1 PPM Overall Frequency Accuracy over an operating temperature range of -40 °C to +85 °C, 3.3V,Sinewave output,TCXO

**NOTE:** Besides **Class R and Class V**, the following additional models are available for applications that can accommodate reduced level of screening and quality conformance inspection:

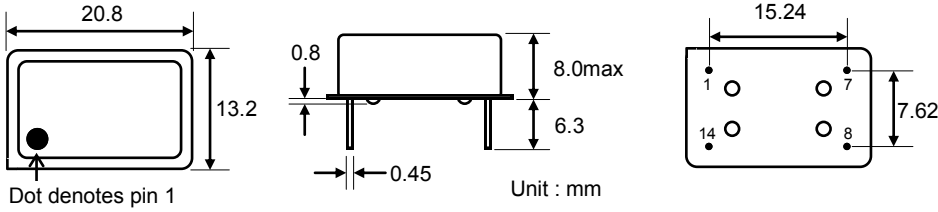
**Class S :** **Class S** is same as **Class R and ClassV** except as follows:  
 Group A inspection is per MIL-PRF-55310, Class B  
 Group B inspection (30 day aging ) per MIL-PRF-55310 is not applicable

**Class B :** **Class B** uses the same design and elements as **Class S** except as follows:  
 100% screening is as per Table III herein  
 PDA for Burn-in is 10% or 1 unit whichever is greater  
 Delta measurements of paragraph 4.3 are not applicable  
 Group A inspection is as per MIL-PRF-55310, Class B  
 Group B inspection (30 day aging ) per MIL-PRF-55310 is not applicable

**Class M :** **Class M** is same as **Class B** except as follows:  
 Active and Passive Elements are as per MIL-PRF-55310, Class B. Microcircuit die is similar to the one used in Class B but is not from radiation tested wafer lot.

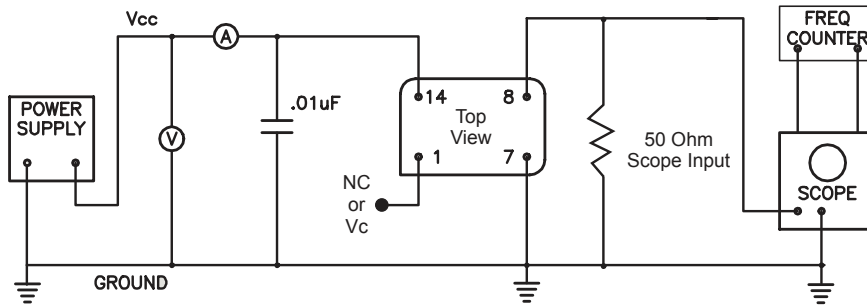
**Class I/N :** **Class I and Class N** is a form, fit and function equivalent prototype of **Class R to Calss M**  
 Prototypes may use and industrial and commercial grade elements and are not screened. Quality Conformance inspection is not applicable.

**Package & Dimensions :**

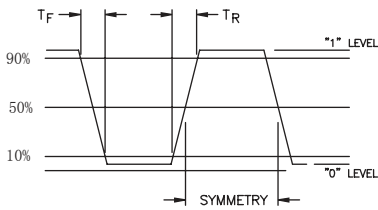


Pin Connections		
	TCXO	VCTCXO
1	N.C	V <sub>C</sub>
7	GND	
8	Output	
14	V <sub>DD</sub>	

**Test Circuit**

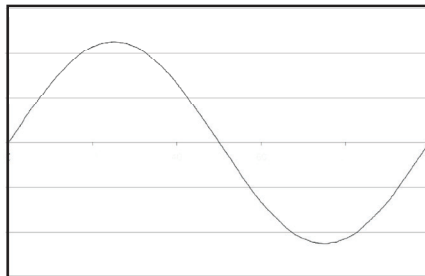


**CMOS Output**

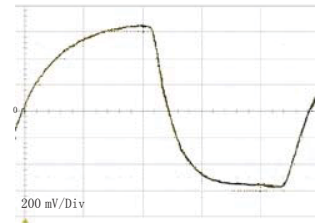


**Sinewave Output**

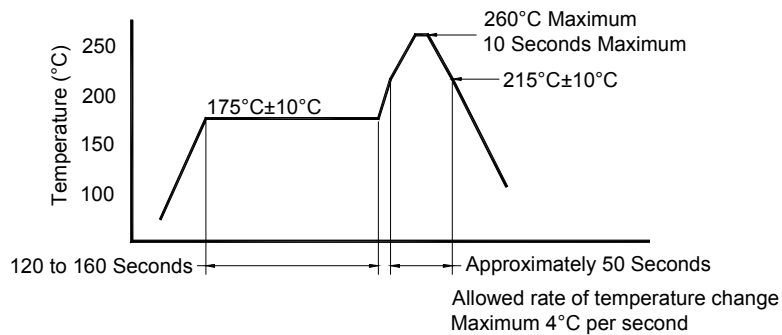
Sinewave Output, 1 dBm minimum into 50 Ohms



**Clipped Sinewave Output**



**Reflow Cycle (typical for lead free processing)**



# FTTC14 Series High Reliability DIP14 Package TCXO/VCTCXO

## Electrical Specifications

<i>Electrical</i>							
Parameter	Symb	Conditions	Min	Typ	Max	Unit	Note
Frequency Range	F	CMOS	0.1		800	MHz	
Input Voltage	Vcc		3.135 4.75	3.30 5.0	3.465 5.25	V	A 0
Input Current	Icc	CMOS			50	mA	@100MHz, 3.3V
Frequency Stab.	ΔF/F	Overall, available			±4.6		20 years
Frequency Stability	ΔF/F	vs. Temperature vs. Vcc aging		±0.5 ±0.1	±1 ±1 ±3.0	ppm ppm/V ppm/year ppm	See chart First Year 20 years
Calibration	ΔF/F	As shipped, 25°C		±0.5	±1	ppm	
Load		CMOS	15pf/10K Ohm				
Duty cycle		@50%	45	50	55	%	CMOS
Rise/Fall time	Tr/Tf	20 to 80 %		3		ns	CMOS
Logic "1" level	Voh	CMOS	0.9Vcc			V	
Logic "0" level	Vol	CMOS			0.1Vcc	V	
Start up time	Ts			2	100	ms	
Phase Jitter		1σ		0.4 0.5	1 1.0	ps	100Hz to 20MHz 12KHz to 20MHz
Subharmonics					none		
Spurious					-80	dBc	
SSB Phase Noise		@10Hz @100 Hz @1 KHz @10 KHz @100 KHz		-90 -120 -140 -150 -152		dBc/Hz	@20MHz

Frequency Range	F	Sine-wave	1		200	MHz	
Input Voltage	Vcc		3.135 4.75	3.30 5.0	3.465 5.25	V	A 0
Input Current	Icc	Sine			40	mA	@100MHz, 3.3V
Frequency Stab.	ΔF/F	Overall, available			±4.6		20 years
Frequency Stability	ΔF/F	vs. Temperature vs. Vcc aging		±0.5 ±0.05	±2 ±1 ±2.0 ±3.0	ppm ppm/V ppm/year ppm	See chart First Year 7 years 20 years
Calibration	ΔF/F	As shipped, 25°C		±0.5	±1	ppm	
Load		Sine	Internally AC-coupled 50 Ohm				
Output power <sup>(2)</sup>	P	Sine-wave Into 50 Ohms	1 3	3 6		dBm	3.3V 5.0V
Start up time	Ts			2	10	ms	
Period Jitter		1σ		3	5	ps	
Phase Jitter				0.5	1.0	ps	12KHz to 20MHz
Subharmonics					none		
Spurious					-80	dBc	
Harmonics		Sine-wave			-30	dBc	
SSB Phase Noise		@10Hz @100 Hz @1 KHz @10 KHz @100 KHz		-90 -120 -140 -150 -152		dBc/Hz	@20 MHz
Input Impedance			>100K Ohm				
Control voltage	Vc		0.3/0.5		3.0/4.5		V(3.3V/5V)
Modulation bandwidth	MB				1.5	Hz	
Deviation	ΔF/F	Vc=0V to 3.3V, 25°C	±5	±7		ppm	

Frequency Range	F	Clipped Sinewave	1		200	MHz	
Input Voltage	Vcc		3.135 4.75	3.30 5.0	3.465 5.25	V	A 0
Input Current	Icc	Sine			40	mA	@100MHz, 3.3V
Output	Load				10KOhm//10pF		
	Level				1V p-p min(5V) 0.8V p-p min(3.3V)		

Table II - Class R/V/S , Screening (100%)

Test - Inspection	Test Method – Condition
Nondestructive Bond Pull	MIL-STD-883, Method 2023
Internal Visual	MIL-STD-883, Method 2017, Level S
Stabilization Bake ( Prior to Seal ) <u>1/</u>	MIL-STD-883, Method 1008, Condition C ( +150 °C ), 48 hours minimum
Thermal Shock	MIL-STD-883, Method 1011, Condition A
Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Constant Acceleration	MIL-STD-883, Method 2001, Condition A Y <sub>1</sub> axis only ( 5000 G )
Seal ( Fine and Gross Leak )	MIL-PRF-55310, Para. 4.8.2.2.2
Particle Impact Noise Detection ( PIND )	MIL-STD-883, Method 2020, Condition A
Radiographic Inspection	MIL-STD-883, Method 2012, Class S
Electrical Tests: Record all measurements.  Input Current Output Frequency Output Voltage Levels Output Rise & Fall Times Output Duty Cycle	Nominal Supply Voltage, Specified load, +23 °C  MIL-PRF-55310, Para. 4.8.5 MIL-PRF-55310, Para. 4.8.6 MIL-PRF-55310, Para. 4.8.21.3 MIL-PRF-55310, Para. 4.8.22 MIL-PRF-55310, Para. 4.8.23
Burn-in ( load )	+125 °C, Nominal Supply Voltage and Burn-in load, 320 Hours Minimum
Electrical Tests: Record all measurements.  Input Current Output Frequency Output Voltage Levels Output Rise & Fall Times Output Duty Cycle	Nominal and Extreme Supply Voltages, Specified load, +23 °C and operating temperature extremes,  MIL-PRF-55310, Para. 4.8.5 MIL-PRF-55310, Para. 4.8.6 MIL-PRF-55310, Para. 4.8.21.3 MIL-PRF-55310, Para. 4.8.22 MIL-PRF-55310, Para. 4.8.23

1/ Vacuum bake and maintain oscillators in dry nitrogen per MIL-PRF-55310.



Table III - Class B/M Screening (100%)

Test - Inspection	Test Method – Condition
Nondestructive Bond Pull	MIL-STD-883, Method 2023
Internal Visual	MIL-STD-883, Method 2017, Level B
Stabilization Bake ( Prior to Seal ) <u>1/</u>	MIL-STD-883, Method 1008, Condition C ( +150 °C ), 24 hours minimum
Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Constant Acceleration	MIL-STD-883, Method 2001, Condition A Y <sub>1</sub> axis only ( 5000 G )
Seal ( Fine and Gross Leak )	MIL-PRF-55310, Para. 4.8.2.2.2
Particle Impact Noise Detection ( PIND )	MIL-STD-883, Method 2020, Condition A
Electrical Tests:  Input Current Output Frequency Output Voltage Levels Output Rise & Fall Times Output Duty Cycle	Nominal Supply Voltage, Specified load, +23 °C Verify all parameters  MIL-PRF-55310, Para. 4.8.5 MIL-PRF-55310, Para. 4.8.6 MIL-PRF-55310, Para. 4.8.21.3 MIL-PRF-55310, Para. 4.8.22 MIL-PRF-55310, Para. 4.8.23
Burn-in ( load )	+125 °C, Nominal Supply Voltage and Burn-in load, 160 Hours Minimum
Electrical Tests:  Input Current Output Frequency Output Voltage Levels Output Rise & Fall Times Output Duty Cycle	Nominal Supply Voltage, Specified load, +23 °C and verify frequency at temperature extremes.  MIL-PRF-55310, Para. 4.8.5 MIL-PRF-55310, Para. 4.8.6 MIL-PRF-55310, Para. 4.8.21.3 MIL-PRF-55310, Para. 4.8.22 MIL-PRF-55310, Para. 4.8.23

1/ Vacuum bake and maintain oscillators in dry nitrogen per MIL-PRF-55310.